Electronic Acknowledgement Receipt FES ID: 1227829 Application Number: 10711532 Confirmation Number: 5531 Avoiding Excessive Cross-terminal Voltages of Low Voltage Transistors Title of Invention: Due to Undesirable Supply-Sequencing in Environments With Higher Supply Voltages First Named Inventor: Bhupendra SHARMA Customer Number: 23494 Filer: Steven A. Shaw/Gracia Sansom Filer Authorized By: Steven A. Shaw Attorney Docket Number: TI-38242 Receipt Date: 29-SEP-2006 Filing Date: 24-SEP-2004 Time Stamp: 15:16:50 Application Type: Utility International Application Number:

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages		
1		T38242-OAR116.pdf	8950	yes	5		
	Multipart Description						
	Doc Desc		Start	End			
	Amendment After Final		1	1			
	Claims		2	4			
	Applicant Arguments/Remarks Made in an Amendment		5	5			
Warnings:							
Information:							
2	Fee Worksheet (PTO-875)	fee-info.pdf	8221	no	2		
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		Total Files Size (in bytes):	17171				

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.